

INFORMATION DISCLOSURE

STATEMENT BY APPLICANT

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Application Number	09/784,928
Filing Date	02/15/2001
First Named Inventor:	Edward L. Schwartz
Art Unit	2621
Examiner Name	Ishrat I. Sherali
Attorney Docket Number	074451.P127

Sheet 1 of 8

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Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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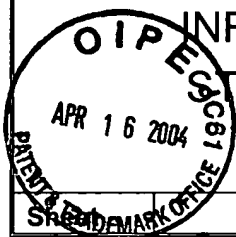
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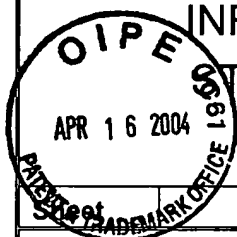
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NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published		
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NON PATENT LITERATURE DOCUMENTS

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known

Application Number	09/784,928
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First Named Inventor:	Edward L. Schwartz
Art Unit	2621
Examiner Name	Ishrat I. Sherali
Attorney Docket Number	074451.P127

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ls		SHAH, et al., "A Chip Set for Lossless Image Compression", <u>IEEE Journal of Solid-State Circuits</u> , Vol. 26, No. 3, March 1991, pp. 237-244.	
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